



# Magnum Series

Size 8 Cavity Optoelectronic PCB Insert, ELIO<sup>®</sup>, Front Release, 850nm  
- Arinc 818, 803 & 804 Compliant

Optical Transmitter Unit

## FEATURES

- Compliant with Arinc 664, 818, 803 & 804
- Suitable for Fast Ethernet, Gigabit Ethernet, 1x/2x/4xFibre Channel and sFPDP applications from 125Mbps to 4.25Gbps
- Maximum optical channel bit error rate less than  $1 \times 10^{-12}$
- Operating temperature range from -55°C to +85°C
- Shock and vibration resistant per RTCA / D0-160E
- Arcap contact insert material meets stringent EMI / RFI / ESD & EMP performance specifications
- Six pin PCB footprint with TX\_Fault and TX\_Dis functions
- ELIO<sup>®</sup> 2.5mm ceramic optical fiber ferrule connector interface per EN 4531\*
- Compatible with Arinc 600 and Mil-Dtl-83527 size 8 (Quadax) insert cavities

## APPLICATIONS

Magnum series printed circuit board mounted optical transmitters enable high speed network communications over long distances in harsh environments.

- Fast or Gigabit Ethernet switches and peripherals
- Fibre Channel switches and peripherals
- Serial Rapid I/O (sRIO) interfaces
- sFPDP data links
- Video displays

This size 8 Optoelectronic cavity insert provides a rugged optical interface that is compliant with ELIO<sup>®</sup> 2.5mm ceramic optical ferrules\*.

The multimode optical fiber interface supports applications where copper cable link distance, bandwidth, weight or bulk make the use of twisted pair, twinax or quadax copper conductors unacceptable.

\*ELIO<sup>®</sup> is a registered trademark of Souriau



ELIO<sup>®</sup> / PCB Mounted\*

## DESCRIPTION

Magnum series Optoelectronic size 8 cavity PCB insert transmitters consist of optoelectronic transmitter functions integrated into a printed circuit board mounted pin contact. The optical transmitters are 850nm VCSEL lasers. The transmitter input lines are driven with differential CML signals applied to the transmitter (TX+ and TX-) lines. Dual loop, temperature compensated, VCSEL drivers convert the transmitter input signals to suitable VCSEL bias and modulation currents. The TX\_Fault circuit disables the optical transmitter output when the optical output power or internal current exceeds predefined limits. The fault condition is latched until reset by a toggle of TX\_Dis or VCC. A CMOS fault signal is generated on the TX\_Fault line upon a transmitter optical or electrical fault condition.

The optical mating interface to the Magnum series size 8 cavity insert optical transmitters is an ELIO<sup>®</sup> 2.5mm ceramic fiber optic ferrule stub per EN 4531. The ferrule stub has an integral 50/125µ multimode optical fiber enabling it to interface to either 62.5/125µ or 50/125µ optical fiber cable.

The electrical interface to the Magnum series size 8 cavity insert optical transmitters is a six position pin header suitable for thru-hole soldering to a flexible or rigid printed circuit. Magnum series optoelectronic size 8 cavity optical transmitters must be mounted in conductive connector inserts.

Magnum series size 8 cavity insert optical transmitters are vibration isolated, environmentally hardened components designed for use in harsh environment applications.

## ORDERING INFORMATION

Application	Part Number
Fast or Gigabit Ethernet, 1x/2xFC	P44F-TS1D-EK
sFPDP / Arinc 818 @ 2.5 to 3.19Gbps	P44F-TS1E-EK
4xFC - 3.2 to 4.25Gbps	P44F-TS1G-EK



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Magnum Series, 2.5mm Ferrule, Size 8 Cavity Insert, Optical Transmitter,  
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### ABSOLUTE MAXIMUM RATINGS

Absolute maximum limits mean that no catastrophic damage will occur if the product is subjected to these ratings for short periods, provided each limiting parameter is in isolation and all other parameters have values within the performance specification. It should not be assumed that limiting values of more than one parameter can be applied to the product at the same time.

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Storage Temperature	$T_s$	-55		+100	°C
Supply Voltage	$V_{CC}$	-0.5		+4.5	V
TX_DIS Input Voltage	$V_I$	-0.5		$V_{CC} + 0.5$	V
Differential Input Voltage (p-p)	$V_D$			2.2	V

### RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Operating Temperature	$T_A$	-55		+85	°C
Power Supply Voltage	$V_{CC}$	+3.135		+3.465	V
TX Differential Input Voltage (p-p)	$V_D$	0.25		2.2	V
Power Supply Noise (p-p)	$N_P$			200	mV

### ENVIRONMENTAL OPERATING CONDITIONS

Requirement	Feature	Condition	Notes
RTCA / D0-160E	ESD	HBM	2200V
RTCA / D0-160E	Vibration	30.0g	18mS
RTCA / D0-160E	Shock	40.0g	6-9mS
RTCA / D0-160E	Flame Resistance		30 Seconds
RTCA / D0-160E	Damp Heat	10 Cycles	24 Hours
ARINC 801	Mating Durability	500 Cycles	<0.5dB Change

### MATERIALS

Item	Detail	Notes
Insert Shell	Arcap	
Solder Pins	Brass	
Solder Pin Plating	Gold over Nickel	
Ferrule	Ceramic	
Printed Circuits	Polyimide / FR-4	

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### OPTICAL TRANSMITTERS $T_A$ = Operating Temperature Range, $V_{CC}$ = 3.135V to 3.465V

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Optical Output Power (BER<math>10^{-12}</math>, PRBS= $2^7-1$ )	$P_o$	-6.5		-1.0	dBm
Optical Output Wavelength	$\lambda_{OUT}$	830	850	860	nm
Spectral Width	$\Delta\lambda_{RMS}$			0.85	nm
Extinction Ratio	ER	9.0	11.0		
Optical Modulation Amplitude (p-p)	OMA	360			$\mu$ W
Total Contributed Jitter (PRBS= $2^7-1$ )	$T_{CJ}$			60	ps

### POWER SUPPLY CURRENT $T_A$ = Operating Temperature Range, $V_{CC}$ = 3.135V to 3.465V

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Supply Current	$I_{CCT}$		50	95	mA

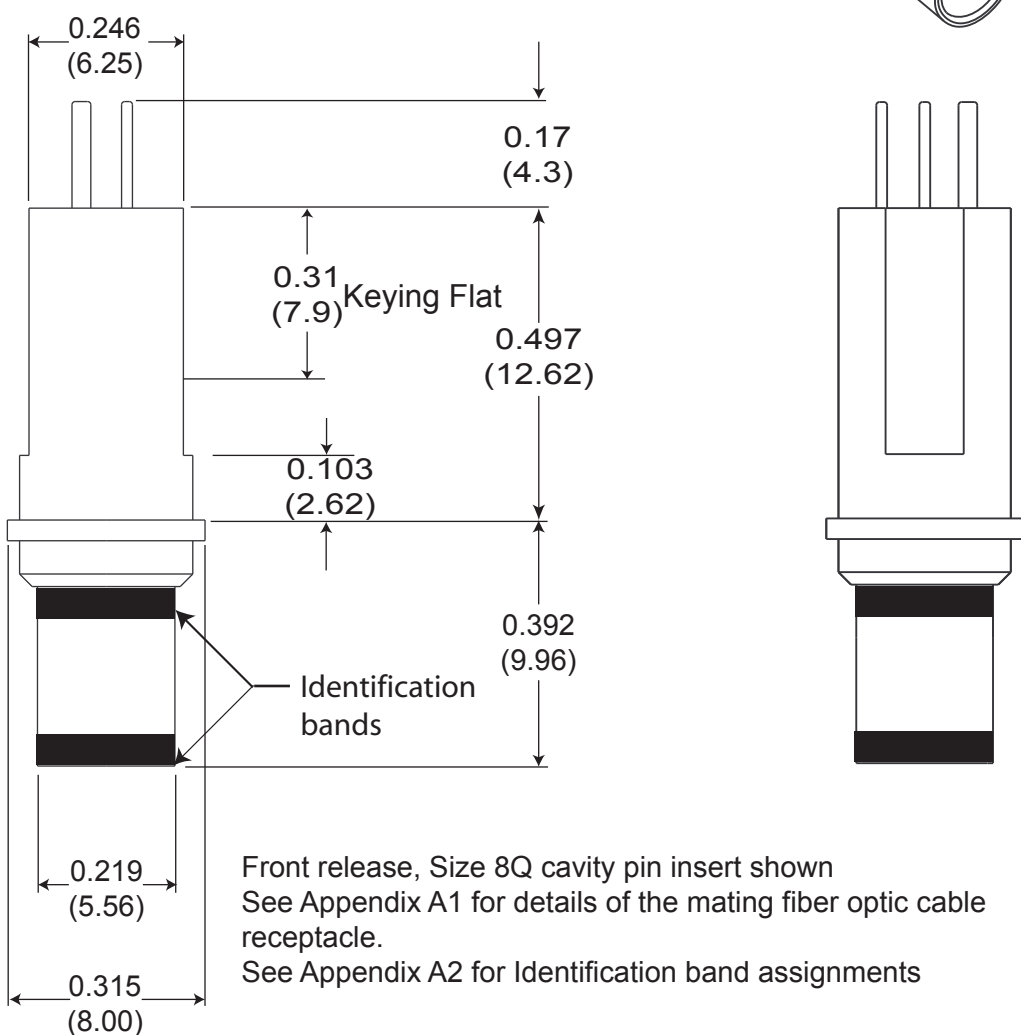
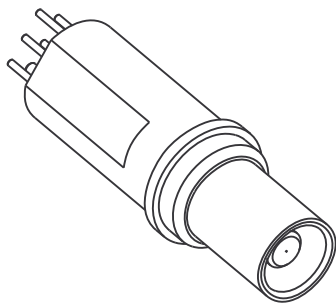
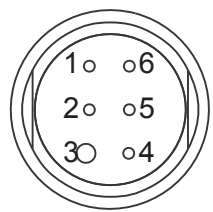


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## OUTLINE DRAWING

Dimensions are shown as: inches (mm)





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### ELECTRICAL PIN ASSIGNMENTS

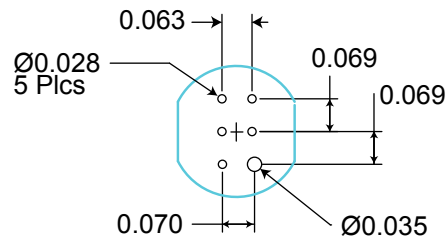
Magnum Size 8 Cavity Insert

Pin Number	Symbol	Description	Logic Family
1	TX_DIS	Transmit Disable - Input Logic 1: Disable Optical Output Logic 0: Enable Optical Output	CMOS Internal 4.7KΩ to 10.0KΩ pullup / pulldown
2	V <sub>cc</sub>	Power Supply	N/A
3	GND	Ground	N/A
4	TX_Fault	Internal TX Fault Indicator - Output Satisfactory Operation: Logic "0" Output Internal Fault: Logic "1" Output	Open Drain CMOS
5	TX-	Transmitter Data Input	CML
6	TX+	Transmitter Data Input	CML

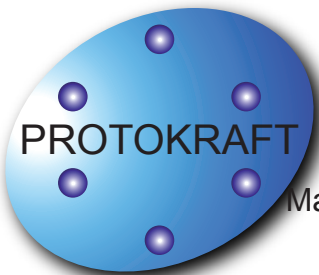
### PRINTED CIRCUIT BOARD FOOTPRINT

Dimensions are shown as: inches

#### PCB Hole Pattern Mounting Side View



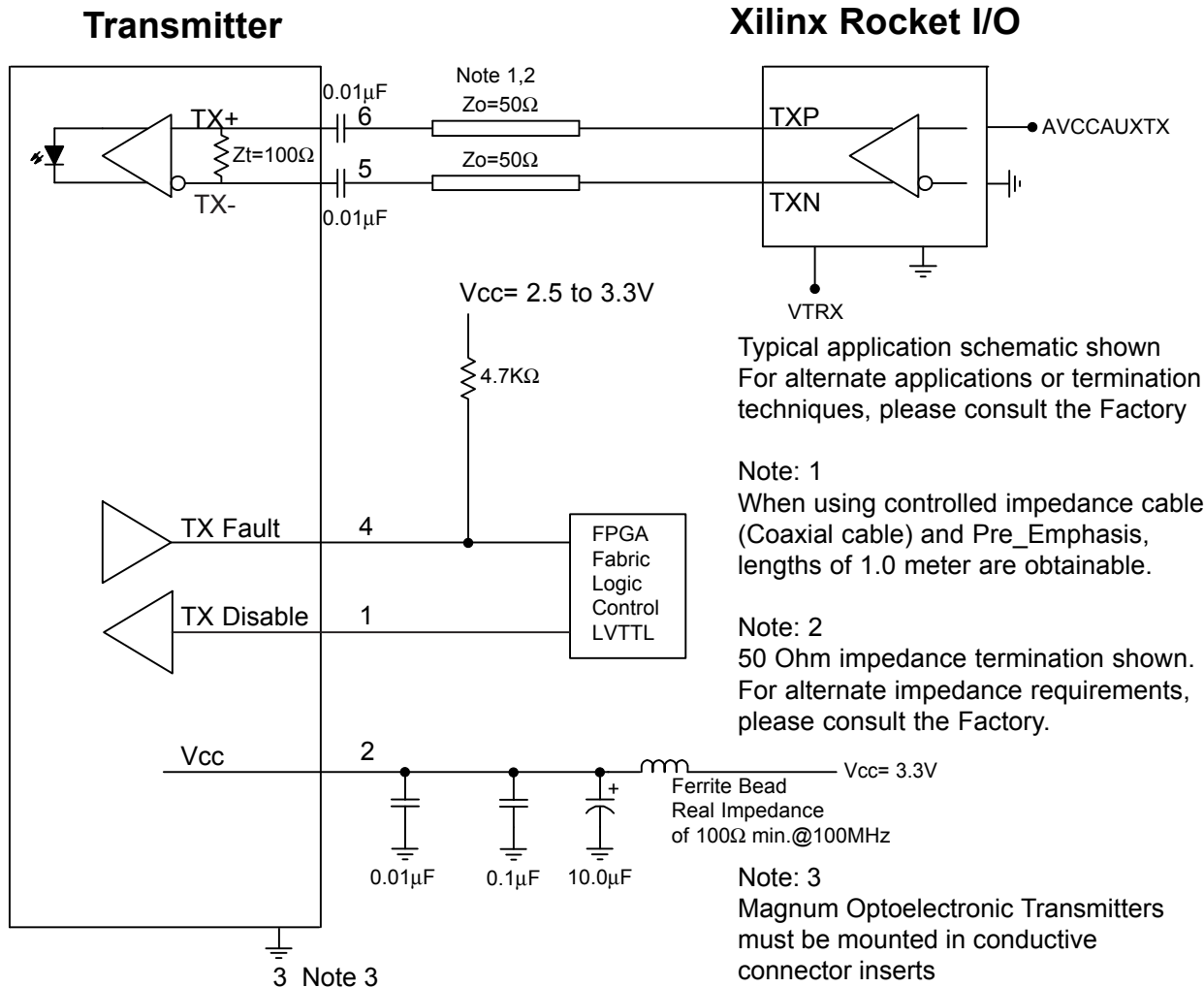
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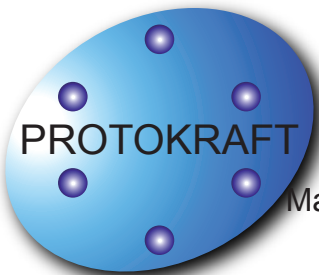
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### APPLICATION SCHEMATIC For Xilinx Rocket I/O Interfaces



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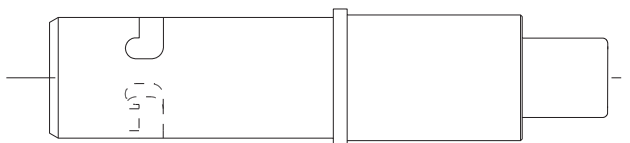
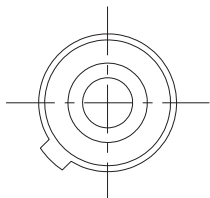
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**APPENDIX A1**  
**SOURIAU ELIO® FIBER OPTIC CABLE ADAPTERS**



**ELIO® 8 Adapter for Quadrax # 8 Cavity in Female Inserts**  
**Ordering Information: ELIO AQ0S**



**ELIO® Termini**

**Ordering Information**

	Ello	09	N	G	L	A
<b>Cable external diameter:</b> 09: 0.9 mm or cable wider than 1.9mm with 0.9mm jacket inside 18: from 1.5mm to 1.9mm						
<b>Contact sealing:</b> W: waterproof (1.8mm +/- 0.1mm cable only) N: non waterproof						
<b>Fibre type:</b> G: 50 or 62,5/125 µm D: 100/140 µm						
<b>Boot type:</b> L: Long boot S: Short boot N: No boot (non waterproof version only)						
<b>Contact version index</b>						

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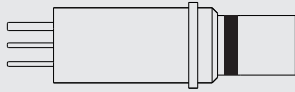
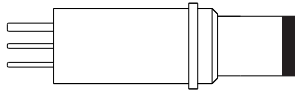
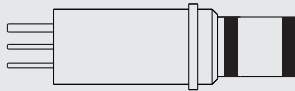
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**APPENDIX A2**

**Identification Band Assignments**

Magnum Size 8 Optoelectronic Cavity Insert

Function	Part Numbers	Identification Band Location	Dust Cap Color
RX - 125Mbps to 2.5Gbps	P44x-RSxD-xx	 <b>No Bands</b>	<b>Blue</b>
RX - 2.5Gbps to 3.2Gbps	P44x-RSxE-xx	 <b>One Band - Base</b>	<b>Black</b>
TX - 125Mbps to 2.5Gbps	P44x-TSxD-xx	 <b>One Band - Tip</b>	<b>Red</b>
TX - 2.5Gbps to 3.2Gbps	P44x-TSxE-xx	 <b>Two Bands - Base &amp; Tip</b>	<b>Clear</b>

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