Magnum Series

Size 8 Cavity Optoelectronic PCB Insert, *ELIO[®], Front Release, 1310nM - Arinc 818, 803 & 804 Compliant

Optical Transmitter Unit

FEATURES

- Compliant with Arinc 664, 818, 803 & 804
- Suitable for Fast Ethernet, Gigabit Ethernet and 1xFibre Channel applications from 50Mbps to 1.5Gbps
- Maximum optical channel bit error rate less than 1x10⁻¹²
- Operating temperature range from -55°C to +85°C
- Shock and vibration resistant per RTCA / D0-160E
- Arcap contact insert material meets stringent EMI / RFI / ESD
 & EMP performance specifications
- Six pin PCB footprint with TX_Fault and TX_Dis functions
- ELIO[®] 2.5mm ceramic optical fiber ferrule connector interface per EN 4531*
- Compatible with Arinc 600 and MIL-DTL-83527 size 8 (Quadrax) insert cavities

APPLICATIONS

Magnum series printed circuit board mounted optical transmitters enable high speed network communications over long distances in harsh environments.

- Fast or Gigabit Ethernet switches and peripherals
- Fibre Channel switches and peripherals
- Serial Rapid I/O (sRIO) interfaces
- PCI Express Links
- sFPDP data links
- Video displays

This size 8 Optoelectronic cavity insert provides a rugged optical interface that is compliant with ELIO[®] 2.5mm ceramic optical ferrules*.

The multimode optical fiber interface supports applications where copper cable link distance, bandwidth, weight or bulk make the use of twisted pair, twinax or quadrax copper conductors unacceptable.

*ELIO® is a registered trademark of Esterline Souriau

US Pat. # 7,690,849



One TX Channel Operating from 50Mbps to 1.5Gbps

DESCRIPTION

Magnum series Optoelectronic size 8 cavity PCB insert transmitters consist of optoelectronic transmitter functions integrated into a printed circuit board mounted pin contact. The optical transmitters are 1310nm FP lasers. The transmitter input lines are driven with differential CML signals applied to the transmitter (TX+ and TX-) lines. Dual loop, temperature compensated, laser drivers convert the transmitter input signals to suitable laser bias and modulation currents. The TX_Fault circuit disables the optical transmitter output when the optical output power or internal current exceeds predefined limits. The fault condition is latched until reset by a toggle of TX_Dis or VCC. A CMOS fault signal is generated on the TX_Fault line upon a transmitter optical or electrical fault condition.

The optical mating interface to the Magnum series size 8 cavity insert optical transmitters is an ELIO[®] 2.5mm ceramic fiber optic ferrule stub per EN 4531. The ferrule stub has an integral $50/125\mu$ multimode optical fiber enabling it to interface to either $62.5/125\mu$ or $50/125\mu$ optical fiber cable.

The electrical interface to the Magnum series size 8 cavity insert optical transmitters is a six position pin header suitable for thruhole soldering to a flexible or rigid printed circuit.

Magnum series size 8 cavity insert optical transmitters are vibration isolated, environmentally hardened components designed for use in harsh environment applications.

ORDERING INFORMATION

| Application | Part Number |
|-------------------|--------------|
| 50Mbps to 1.5Gbps | P44F-TL1D-EK |



ABSOLUTE MAXIMUM RATINGS

Absolute maximum limits mean that no catastrophic damage will occur if the product is subjected to these ratings for short periods, provided each limiting parameter is in isolation and all other parameters have values within the performance specification. It should not be assumed that limiting values of more than one parameter can be applied to the product at the same time.

| Parameter | Symbol | Minimum | Typical | Maximum | Unit |
|----------------------------------|-----------------|---------|---------|-----------------------|------|
| Storage Temperature | T _s | -55 | | +100 | °C |
| Supply Voltage | V _{cc} | -0.5 | | +4.5 | V |
| TX_DIS Input Voltage | V | -0.5 | | V _{cc} + 0.5 | V |
| Differential Input Voltage (p-p) | V _D | | | 2.2 | V |

RECOMMENDED OPERATING CONDITIONS

| Parameter | Symbol | Minimum | Typical | Maximum | Unit |
|-------------------------------------|-----------------|---------|---------|---------|------|
| Operating Temperature | T _A | -55 | | +85 | °C |
| Power Supply Voltage | V _{cc} | +3.135 | | +3.465 | V |
| TX Differential Input Voltage (p-p) | V _D | 0.25 | | 2.2 | V |
| Power Supply Noise (p-p) | N _P | | | 200 | mV |

ENVIRONMENTAL OPERATING CONDITIONS

| Requirement | Feature | Condition | Notes |
|------------------------|-------------------|------------|-------------------------------|
| RTCA / D0-160E | ESD | HBM | 2200V |
| RTCA / D0-160E | Vibration | 3.8g²/Hz | 43G rms |
| RTCA / D0-160E | Shock | 40.0g | 6-9mS |
| RTCA / D0-160E | Flame Resistance | | 30 Seconds |
| RTCA / D0-160E | Damp Heat | 10 Cycles | 24 Hours |
| ARINC 801 | Mating Durability | 500 Cycles | <0.5dB Change |
| FDA / CDRH / IEC-825-1 | Eye Safety | Class 1 | No Safety Interlocks Required |

MATERIALS

| Item | Detail | Notes |
|--------------------|------------------|-------|
| Insert Shell | Arcap | |
| Solder Pins | Brass | |
| Solder Pin Plating | Gold over Nickel | |
| Ferrule | Ceramic | |
| Printed Circuits | Polyimide / FR-4 | |

OPTICAL TRANSMITTERS T_A = Operating Temperature Range, V_{cc} = 3.135V to 3.465V

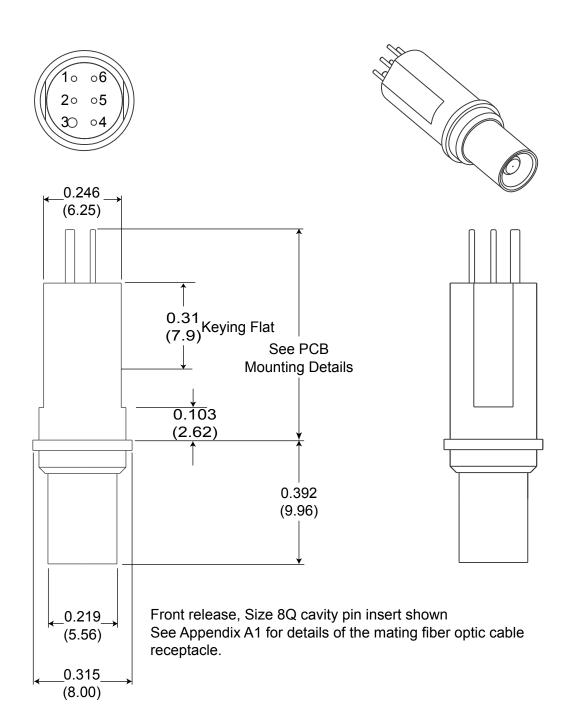
| Parameter | Symbol | Minimum | Typical | Maximum | Unit |
|--|------------------------|---------|---------|---------|------|
| Optical Output Power (ER>9.0, BER<10 ⁻¹²)* | P _o | -9.5 | | -1.0 | dBm |
| Optical Output Wavelength | λ_{OUT} | 1270 | | 1340 | nM |

POWER SUPPLY CURRENT T_A = Operating Temperature Range, V_{cc} = 3.135V to 3.465V

| Parameter | Symbol | Minimum | Typical | Maximum | Unit |
|----------------|------------------|---------|---------|---------|------|
| Supply Current | I _{сст} | | 50 | 95 | mA |

OUTLINE DRAWING

Dimensions are shown as: inches (mm)



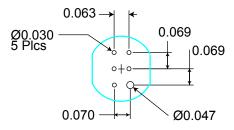
| Pin Number | Symbol | Description | Logic Family | | | |
|------------|-----------------|--|---------------------------------|--|--|--|
| 1 | TX_DIS | Transmit Disable - Input Logic 1: Disable Optical Output Logic 0: Enable Optical Output | CMOS Internal 4.7KΩ pulldown | | | |
| 2 | V _{cc} | Power Supply | N/A | | | |
| 3 | GND | Ground | N/A | | | |
| 4 | TX_Fault | Internal TX Fault Indicator - Output Satisfactory Operation: Logic "0" Output Internal Fault: Logic "1" Output | Open Drain CMOS | | | |
| 5 | TX- | Transmitter Data Input | CML | | | |
| 6 | TX+ | Transmitter Data Input | CML | | | |

ELECTRICAL PIN ASSIGNMENTS

PRINTED CIRCUIT BOARD FOOTPRINT

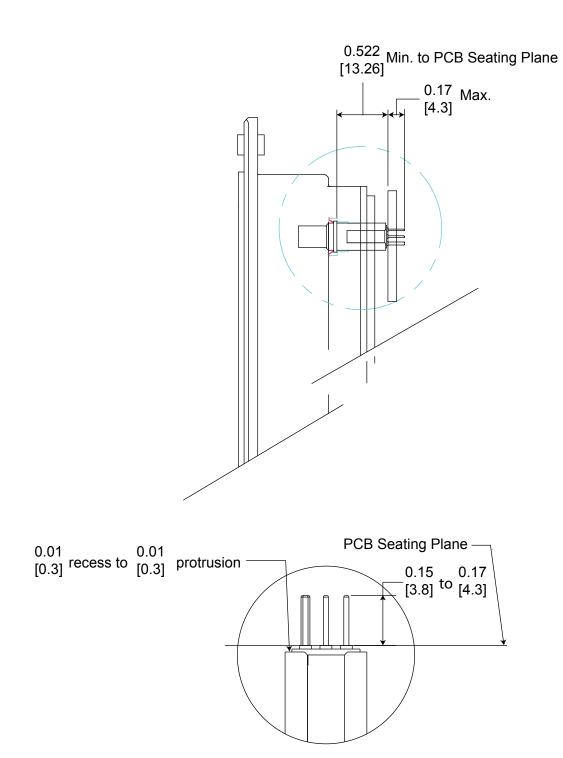
Dimensions are shown as: inches

PCB Hole Pattern Mounting Side View



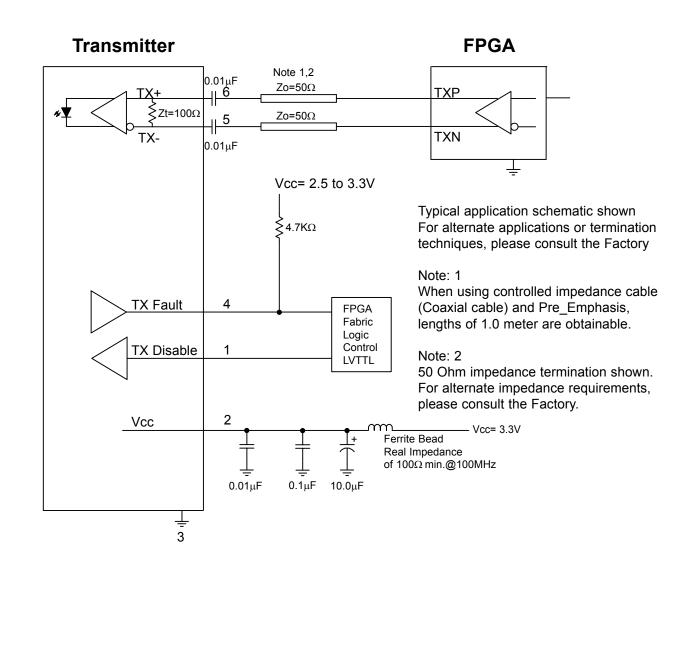
PCB MOUNTING DETAILS

Dimensions are shown as: inches [mm]



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APPLICATION SCHEMATIC For Xilinx Rocket I/O Interfaces

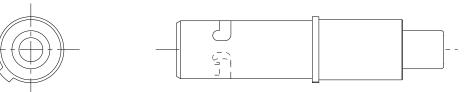


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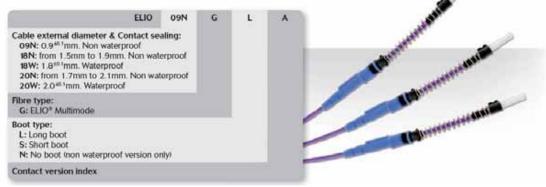
APPENDIX A1 SOURIAU ELIO[®] FIBER OPTIC CABLE ADAPTERS



ELIO[®] 8 Adapter for Quadrax # 8 Cavity in Female Inserts Ordering Information: ELIO AQ0S



ELIO[®] multimode contact Ordering information





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